### UNIVERSITY OF KASHMIR

**Date Sheet**
for

B.E 3rd Semester Backlog Examination (Batch: 2010-2014) of S.S.M College of Engineering, Parihaspora, Pattan. (Session: July-August, 2019).

**VENUE:** S.S.M College of Engineering, Parihaspora, Pattan

- **Time:** 11.00 (a.m.)
- **Friday:** 10.00 (a.m.)

<table>
<thead>
<tr>
<th>Day and Date</th>
<th>Branch</th>
<th>Courses</th>
</tr>
</thead>
</table>
Network Analysis  
Mechanics of Solids-I  
Electronics-I  
Principles of Electrical Engineering |
Basic Electronics  
Principles of Electrical Engineering  
Network Analysis |
Principles of Electrical Engineering  
Production Engineering-I  
Digital Electronics & Logic Design  
Electronics-I |
Electronics Engineering Materials  
Machine Drawing  
C Programming |
|---------------------|---------------------------------------------------------------------------------|---------------------------------------------------------------------------------|

**No:** F (DS—B.E 3rd Sem Exam) KU/19

**Dated:** 24/07/2019

Assistant Controller of Examinations  
*Professional Conduct*

Copy to:-

- Principal, S.S.M College of Engineering, Parihaspora, Pattan for information and necessary action;
- Incharge System Engineer for information and necessary action;  
  Deputy Controller of Examinations, University of Kashmir;
- Assistant Controller (Examination Stationery/Secrecy/Transit) University of Kashmir;
- Media Coordinator for wide publicity through print media;
- Sr. P.A to CE for kind information of the Controller of Examinations;
- Enquiry Cell/ Notice Board /File.